

Please amend the claims as follows (all pending claims are shown below, with only those being amended shown in the attached Version With Markings To Show Changes Made):

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1. (ONCE AMENDED) A multistage amplifier, comprising:

a plurality of amplifying elements for amplifying an input signal stage by stage and outputting an amplified signal; and

a matching circuit, arranged between each pair of amplifying elements adjacent to each other, for performing an impedance matching between the pair of amplifying elements,

wherein one of the matching circuits comprises:

a one-stage high pass filter type matching unit having a parallel inductor and a serial capacitor; and

a one-stage low pass filter type matching unit serially connected with the one-stage high pass filter type matching unit.

2. (ONCE AMENDED) A multistage amplifier, comprising:

a plurality of amplifying elements for amplifying an input signal stage by stage and outputting an amplified signal; and

a matching circuit, arranged between each pair of amplifying elements adjacent to each other, for performing an impedance matching between the pair of amplifying elements,

wherein the matching circuit arranged between the final-stage

amplifying element and the amplifying element placed just before the final-stage amplifying element comprises:

a one-stage high pass filter type matching unit having a parallel inductor and a serial capacitor; and

a one-stage low pass filter type matching unit serially connected with each other.

3. A multistage amplifier according to claim 1, wherein the one-stage high pass filter type matching unit is placed on an input side of the input signal, and the one-stage low pass filter type matching unit is placed on an output side of the amplified signal.

4. A multistage amplifier according to claim 1, wherein the one-stage low pass filter type matching unit is placed on an input side of the input signal, and the one-stage high pass filter type matching unit is placed on an output side of the amplified signal.

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6. (ONCE AMENDED) A multistage amplifier according to claim 1, wherein a bias supply short stub having a length equal to or shorter than $1/4$ of a wavelength of the input signal is used as the

parallel inductor.

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7. A multistage amplifier according to claim 1, wherein the one-stage low pass filter type matching unit comprises a parallel capacitor and a serial inductor.

8. A multistage amplifier according to claim 7, wherein a serial line is used as the serial inductor.

Please add the following new claims:

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9. (NEW) A multistage amplifier according to claim 2, wherein the one-stage high pass filter type matching unit is placed on an input side of the input signal, and the one-stage low pass filter type matching unit is placed on an output side of the amplified signal.

10. (NEW) A multistage amplifier according to claim 2, wherein the one-stage low pass filter type matching unit is placed on an input side of the input signal, and the one-stage high pass filter type matching unit is placed on an output side of the amplified signal.

11. (NEW) A multistage amplifier according to claim 2, wherein a

bias supply short stub having a length equal to or shorter than $1/4$ of a wavelength of the input signal is used as the parallel inductor.

12. (NEW) A multistage amplifier according to claim 2, wherein the one-stage low pass filter type matching unit comprises a parallel capacitor and a serial inductor.

13. (NEW) A multistage amplifier according to claim 12, wherein a serial line is used as the serial inductor.
